

AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0340/P340

Application No.
09/652,003-Conf. #2935

Filing Date
August 31, 2000

Examiner
W. H. Choi

Art Unit
2186

Applicant(s): Graham Kirsch

Invention: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL
PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

TO THE COMMISSIONER FOR PATENTS

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Transmitted herewith is an amendment in the above-identified application.

APR 19 2004

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CLAIMS AS AMENDED

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	45	- 48 =		x	0.00
Independent Claims	15	- 15 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

☒ Large Entity

☐ Small Entity

☒ No additional fee is required for this amendment.

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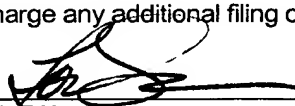
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Thomas J. D'Amico
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Dated: April 13, 2004

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C.B.
4/27/04

Docket No.: M4065.0340/P340
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Graham Kirsch

Confirmation No.: 2935

Application No.: 09/652,003

Art Unit: 2186

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For: METHOD AND APPARATUS FOR
CONNECTING A MASSIVELY PARALLEL
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AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated January 13, 2004 (Paper No. 17), please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 28 of this paper.